

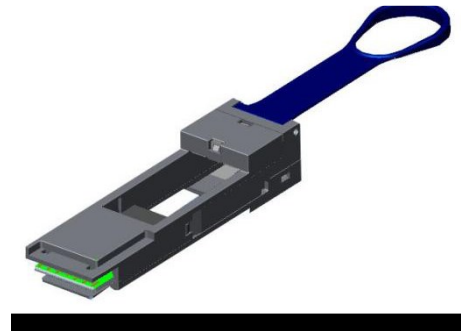
QSFPxx-SFPxx-CONV-x

Multirate Converter for QSFP+ and QSFP28 slot to a single SFP28, SFP+ or SFP slot

This Optomark Converter allows you to use any single SFP28, SFP+ or SFP module in Lane 1 of 4 vacant QSFP28 or QSFP+ ports. Lanes 2-4 are disconnected.

Normal 1G 10G and 28G SFP modules, DAC / AOC can be used if the Host Switch supports the data rates on Lane 1. DWDM and CWDM as well. (please take care of this)

Its your cost-effective stepping stone to 100GE or 40G using 1, 10 and 28GE links in the meantime.



Product Features

- Easy installation and network bring-up
- Industry Compliant SFF-8665 & -8432
- Precision process control for minimization of pair-to-pair skew
- 25Gbps single channel duplex, also 10Gbps, 5Gbps data rates
- EMI performance ensured with All-metal housing
- 100ohm differential impedance system
- Op temp -20 to 85°C
- Low insertion loss & X-talk
- RoHS compliant

Applications

- Switches with QSFP28 or QSFP slots where SFP28 use is desired
- Conversion of QSFP28 slots to a SFP+ / SFP28 CWDM DWDM System

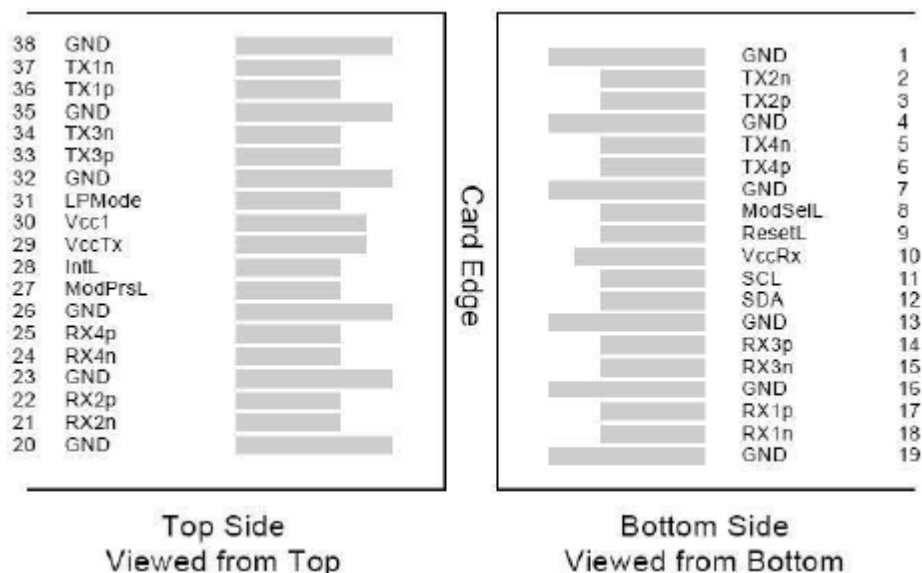
Type	Host	Client
QSFP28-SFP-Conv-xx	QSFP 28	SFP28 or SFP+ or SFP
QSFP-SFP-Conv-xx	QSFP+ 40G	SFP+ or SFP (GbE)

SPECIFICATIONS

Operating Conditions

Parameter	Symbol	Minimum	Maximum	Unit	Notes
Storage Temperature	T _s	-40	85	°C	
Operating Case Temperature	T _{case}	-20	85	°C	
Relative Humidity (non-condensation)	RH	5	85	%	
Supply Voltage	V _{CC3}	3,15	3,45	V	
Data Rate	Gb/s	1		25	

QSFP MSA Connector



SFP MSA Connector



Pin	Logic	Symbol	Name/Description	Note
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	
7		GND	Ground	1
8	LVTLL-I	ModSelL	Module Select	
9	LVTLL-I	ResetL	Module Reset	
10		VccRx	+ 3.3V Power Supply Receiver	2
11	LVC MOS-I/O	SCL	2-Wire Serial Interface Clock	
12	LVC MOS-I/O	SDA	2-Wire Serial Interface Data	
13		GND	Ground	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	1
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3 V Power Supply transmitter	2
30		Vcc1	+3.3 V Power Supply	2
31	LVTTL-I	LPMode	Low Power Mode	
32		GND	Ground	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Output	
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Output	
38		GND	Ground	1

In accordance with the Optomark policy of continuous improvement specifications may change without notice. Further details are available from Optomark representative.

Pin	Logic	Symbol	Name/Description	Note
1		VeeT	Module Transmitter Ground	1
2	LVTTL-O	Tx_Fault	Transmitter Fault	2
3	LVTTL-I	Tx_Disable	Transmitter Disable	3
4	LVTTL-I/O	SDA	MOD-DEF2 2-wire serial interface data line	4
5	LVTTL-I/O	SCL	MOD-DEF1 2-wire serial interface clock line	4
6		Mod_Abs	Module Absent	5
7	LVTTL-I	RS0	Rate Select Zero	
8	LVTTL- O	Rx_LOS	Module Receiver Loss of Signal	2
9	LVTTL-I	RS1	Rate Select One	
10		VeeR	Module Receiver Ground	1
11		VeeR	Module Receiver Ground	1
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Non-Inverted Data Output	
14		VeeR	Module Receiver Ground	1
15		VccR	Module Receiver 3.3V Supply	
16		VccT	Module Transmitter 3.3V Supply	
17		VeeT	Module Transmitter Ground	1
18	CML-I	TD+	Transmitter Non-Inverted Data Input	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	1

Maximum physical dimensions (H x W x D)	13.5 x 18.4 x 78 mm
Weight	90 g

